



SSC80A4GS6

N-Channel Enhancement Mode MOSFET

> Features

V _{DS}	V _{GS}	R _{DS(ON)} Typ.	ID
100V	±20V	240mΩ@10V	2.2A
		260mΩ@4V5	2.27

> Description

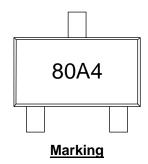
This device uses advanced trench technology to provide excellent RDSON and low gate charge. This device is suitable for use as a load switch or in PWM applications.

> Applications

- Intelligent Lighting
- Load Switch
- Portable Devices
- DCDC Conversion

> Ordering Information

Device	Package	Shipping
SSC80A4GS6	SOT-23	3000/Reel





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S

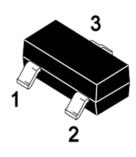
Pin Configuration (Top View)

1

G

1 / 6 Analog Future

Pin configuration



<u>SOT-23</u>

D

3





Symbol	Parameter	Ratings	Unit
V _{DSS}	Drain-to-Source Voltage	100	V
V _{GSS}	Gate-to-Source Voltage	±20	V
ID	Continuous Drain Current ^a	2.2	А
I _{DM}	Pulsed Drain Current ^b	8.8	А
PD	Power Dissipation °	1.76	W
TJ	Operation junction temperature -55~150		°C
T _{STG}	Storage temperature range	-55~150	°C

> Absolute Maximum Ratings ($T_A=25^{\circ}$ unless otherwise noted)

> Thermal Resistance Ratings ($T_A=25^{\circ}C$ unless otherwise noted)

Symbol	Parameter	Maximum	Unit
R _{0JA}	Junction-to-Ambient Thermal Resistance ^a	71	°C/W

Note:

- a. The value of R_{θJA} is measured with the device mounted on 1 in² FR-4 board with 2oz.copper, in a still air environment with T_A=25 °C.The value in any given application depends on the user is specific board design. The power dissipation is based on the t≤10s thermal resistance rating.
- b. Repetitive rating, pulse width limited by junction temperature.
- c. The power dissipation P_D is based on T_{J(MAX)}=150°C, using junction-to-case thermal resistance, and is more useful in setting the upper dissipation limit for cases where additional heat sinking is used.





> Electrical Characteristics (T_A=25 $^{\circ}$ C unless otherwise noted)

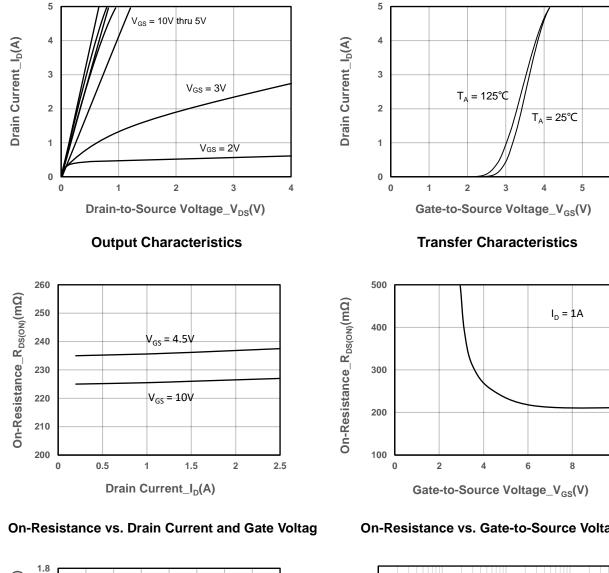
Parameter	Symbol	Test Conditions	Min.	Тур.	Max.	Unit
Drain-Source Breakdown Voltage	V _{(BR)DSS}	$V_{GS} = 0V, I_D = 250 \mu A$	100			V
Gate Threshold Voltage	$V_{GS(th)}$	$V_{DS} = V_{GS}, I_D = 250 uA$	1	1.5	2.5	V
	RDS(on)	$V_{GS} = 10V, I_D = 2A$		240	300	mΩ
Drain-Source On-Resistance		V _{GS} = 4.5V, I _D = 1A		260	330	
Zero Gate Voltage Drain Current	IDSS	$V_{\text{DS}} = 100 \text{V}, V_{\text{GS}} = 0 \text{V}$			1	μA
Gate-Source Leak Current	lgss	$V_{GS} = \pm 20 V$, $V_{DS} = 0 V$			±100	nA
Forward Voltage	V _{SD}	$V_{GS} = 0V, I_{S} = 1A$			1.3	V
Input Capacitance	Ciss			320		pF
Output Capacitance	Coss	$V_{DS} = 25V, V_{GS} = 0V,$		20		
Reverse Transfer Capacitance	Crss	f = 1MHz		14		
Turn-on Delay Time	T _{D(ON)}			14		
Rise Time	Tr	V _{GS} = 10V, I _D = 1A,		53		- ns
Turn-off Delay Time	T _{D(OFF)}	$V_{DS} = 30V, R_G = 3\Omega$		17		
Fall Time	T _f			11		
Total Gate Charge	Q_{G}			5.2		
Gate to Source Charge	Q_{GS}	$V_{GS} = 10V, V_{DS} = 30V,$		1.2		nC
Gate to Drain Charge	Q _{GD}	$I_D = 2A$		1.6		

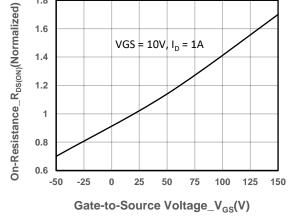


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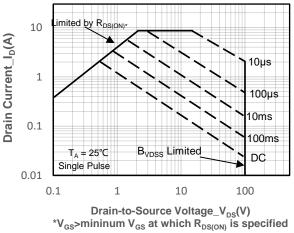
Typical Performance Characteristics (T_A=25℃ unless otherwise noted) \triangleright







On-Resistance vs. Gate-to-Source Voltage

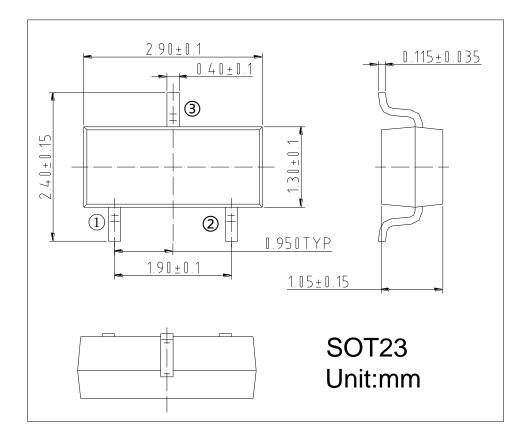


Safe Operating Area vs. Junction-to-Ambient

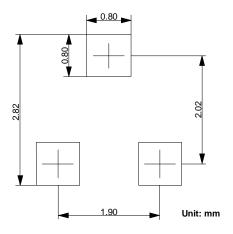




> Package Information



Recommended Pad outline





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